

MRH570.16-416F3

Thyristor/Diode module

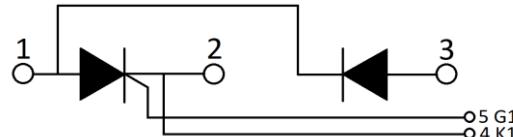
Features:

- Pressure contact technology with increased power cycling capability
- Glass passivated chip
- Simple mounting
- UL recognized, file no. E312789

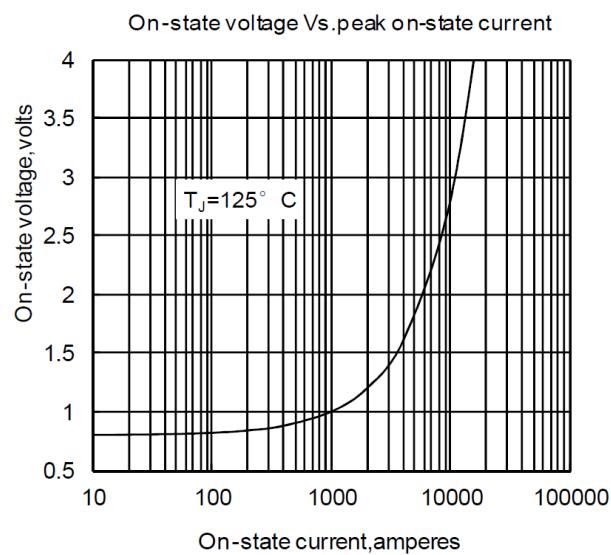
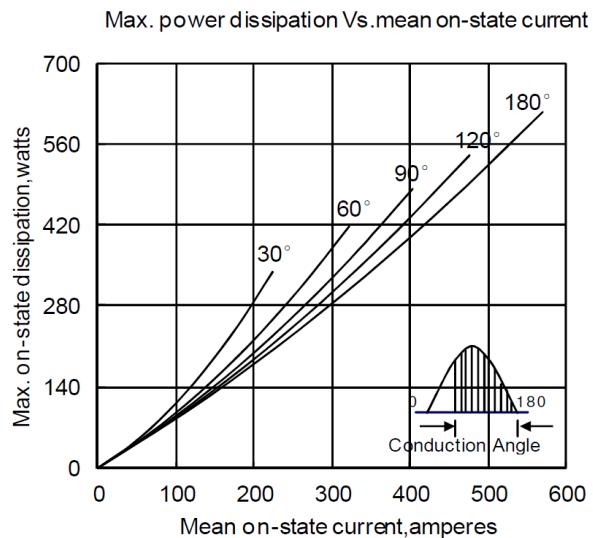
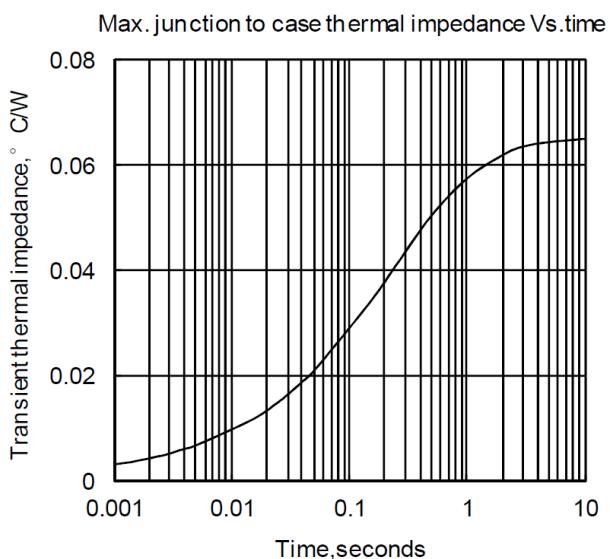
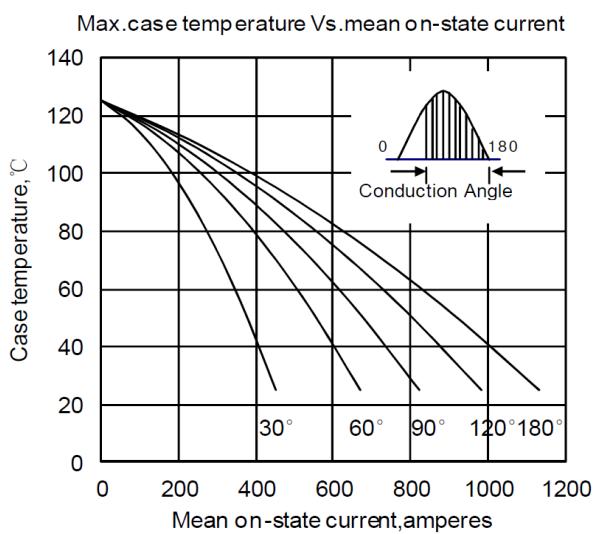
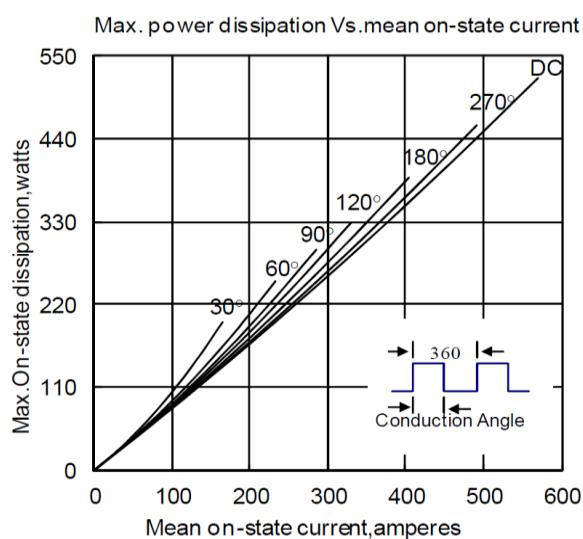
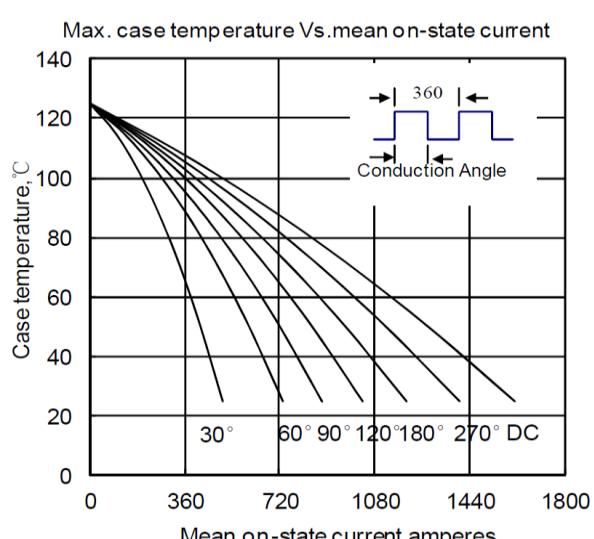


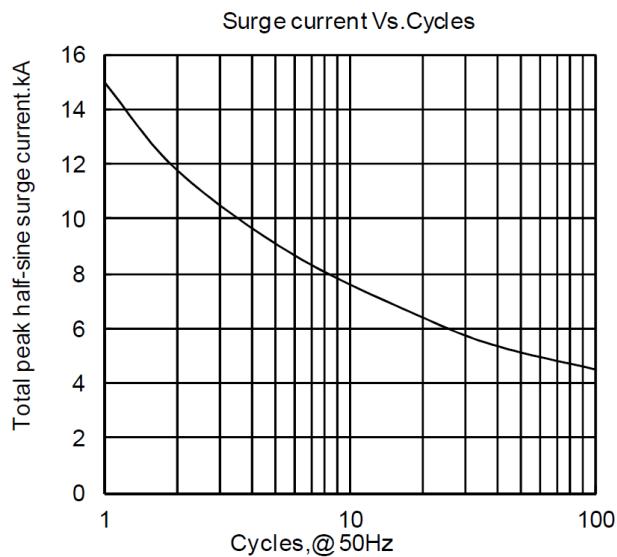
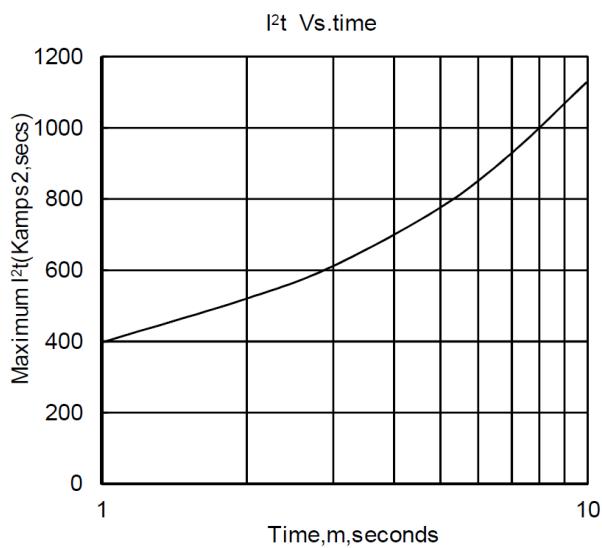
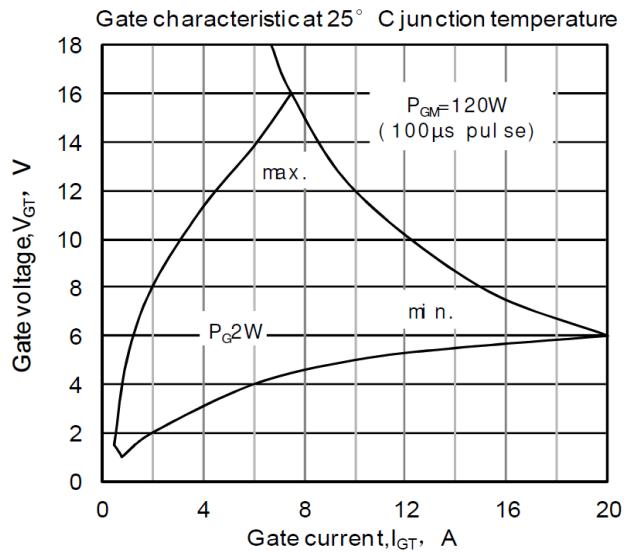
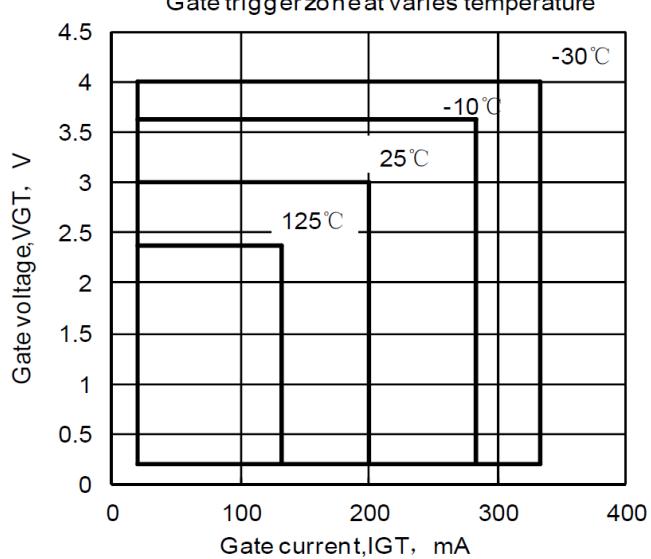
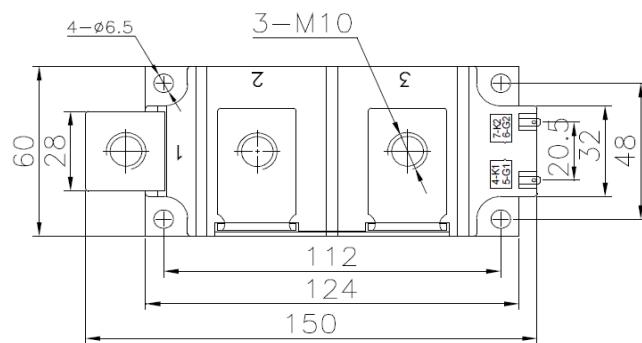
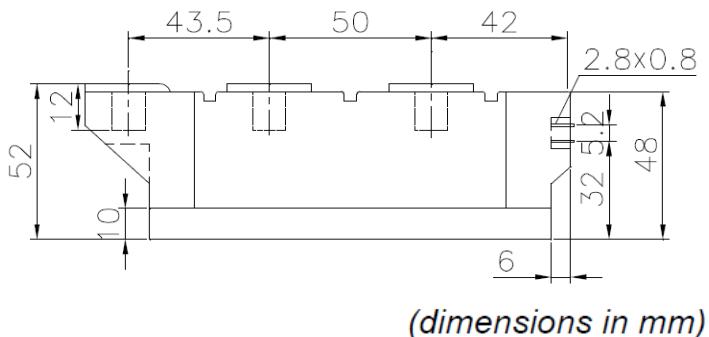
Typical applications:

- Power converters
- Lighting control
- DC motor control and drives
- Heat and temperature control



Symbol	Characteristics	Test Conditions	Value			Unit
			Min	Typ	Max	
$V_{RSM/DSM}$	Non-repetitive reverse/forward blocking voltage	$T_j = 25^\circ\text{C}$			1700	V
$V_{RRM/DRM}$	Repetitive reverse/forward blocking voltage	$T_j = 25^\circ\text{C}$			1600	V
$I_T \cdot I_{F(AV)}$	On-state/forward average current	180° half sine wave 50Hz $T_c = 85^\circ\text{C}$			570	A
$I_T \cdot I_{F(RMS)}$	RMS on-state current				895	A
I_{RRM} I_{DRM}	Repetitive peak current	at V_{DRM}/V_{RRM} $T_j = 125^\circ\text{C}$			35	mA
$I_{TSM} \cdot I_{FSM}$	Surge non repetitive current	10ms half sine wave $T_j = 125^\circ\text{C}$			15000	A
I^2t	I^2t for fusing coordination	10ms half sine wave $V_R = 60\% V_{RRM}$ $T_j = 125^\circ\text{C}$			1125000	A^2s
V_{TO}	Threshold voltage	$T_j = 125^\circ\text{C}$			0.80	V
r_T	On-state slope resistance	$T_j = 125^\circ\text{C}$			0.20	$\text{m}\Omega$
$V_{TM} \cdot V_{FM}$	Thyristor: Peak on-state voltage	$T=25^\circ\text{C}$; $I_{T-F}=750\text{A}$			1.45	V
dv/dt	Critical rate of rise of off-state voltage	$V_{DM} = 67\% V_{DRM}$, $T_j = 125^\circ\text{C}$, linear voltage rise			1000	$\text{V}/\mu\text{s}$
di/dt	Critical rate of rise of off-state current	$T_j = 125^\circ\text{C}$, Gate source 500mA, $T_r < 0.5\mu\text{s}$ Repetitive			200	$\text{A}/\mu\text{s}$
I_{GT}	Gate trigger current	$V_A = 12\text{V}$, $T_j = 25^\circ\text{C}$	30		200	mA
V_{GT}	Gate trigger voltage		1.0		3.0	V
I_H	Holding current	$T_j = 25^\circ\text{C}$, $V_D = 6\text{V}$	20		200	mA
$R_{th(j-c)}$	Thermal resistance junction to case	Single side cooled per chip			0.065	$^\circ\text{C}/\text{W}$
$R_{th(c-s)}$	Thermal resistance case to sink	Single side cooled per chip			0.024	$^\circ\text{C}/\text{W}$
V_{ISO}	Isolation voltage	50Hz, RMS, $t = 1\text{min}$, $I_{ISO} : 1\text{mA (MAX)}$	3000			V
F_M	Mounting torque - copper plate (M6)		4.5		6.0	$\text{N}\cdot\text{m}$
	Mounting torque - terminal (M8)		10.0		12.0	$\text{N}\cdot\text{m}$
T_{stg}	Storage Temperature		-40		125	$^\circ\text{C}$
T_j	Operating Temperature		-40		125	$^\circ\text{C}$
W_t	Weight			1540		g
Outline			416F3			

MRH570.16-416F3

Fig.1

Fig.3

Fig.2

Fig.4

Fig.5

Fig.6

MRH570.16-416F3

Fig. 7

Fig.8

Fig.9

Fig.10

S.C.O.M.E.S. Srl

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